Sean Collins
Backend Software Engineer

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Education

Georgia Institute of Technology (Georgia Tech) *Atlanta, GA*
B.S. Computer Science, Chinese minor *Aug 2012 — Dec 2015*
- Specialized in computer architecture, systems, and networking.
- Studied at Shanghai Jiaotong University as part of a business and technology Chinese immersion program.

Experience

LinkedIn *Sunnyvale, CA*
Site Reliability Engineer *Feb 2017 — Present*
- Point-of-contact for DBAs and engineers to build and maintain automation across LinkedIn's diverse data infrastructure.
- Engineer for production tooling around cross-colo data validation, replication, and access.

Spiceworks *Austin, TX*
DevOps Engineer *Aug 2016 — Jan 2017*
- Team CI/build engineer for build automation, signing, and deployment (Docker, GitLab CI, Kubernetes, Kibana, Vagrant, Node.js, Bash, Ruby).
- First responder for issues in 11 production containers (Kubernetes, GitLab CI, Ember, Node.js, Elixir, Phoenix).

Spiceworks *Austin, TX*
Software Developer *May 2015 — Aug 2016*
- Lead backend engineer for network monitoring application (C#, PowerShell, JavaScript, Ruby, SQLite, Redis, InfluxDB).
- Point-of-contact for operations and deployment duties (Jenkins, Hyper-V, PowerShell, Batch).
- Backend engineer for deployed system inventory application (C#, PowerShell).
- Backend engineer for suite of online web tools (Ember, JavaScript, Node.js).

Selected Personal Projects

YourTunes file system
C, Linux kernel, Python *Fall 2015*
A networked FUSE filesystem optimized for songs with metadata. Music placed in a YourTunes mountpoint is accessible via an indexable hierarchy based on ID3 tags. File I/O is handled by interfacing with a RESTful server-side API.

RedMessenger message push server
Redis, JavaScript (Node.js) *Summer 2015*
A Node.js proxy between a group of Redis publish/subscribe channels and an HTTP WebSocket which allows push notifications to be easily delivered and displayed on a web application in real time.

Niu32 ISA and assembler
Python, Verilog *Summer 2015*
A specification and assembler for a simple, 32-bit RISC instruction set architecture.

Tiger/MIPS compiler
ANTLR, Java, MIPS *Fall 2014*
An LL(1) 3-stage compiler for a conceptualized language called Tiger, compiled to the MIPS architecture.

Certifications and awards

Certifications

2013	**Cisco Certified Network Professional (CCNP)**	Routing and Switching
2013	**Microsoft Technical Associate**	IT Infrastructure
2012	**CompTIA A+**	

Awards

2015	**Dean's List**	Georgia Tech
2014	**First Place**	Georgia Tech—IEEE Cisco Networking Competition